

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Application No.: 10/780,031 Confirmation No.: 9275  
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004  
Group Art Unit: 2824 Examiner: Wendler, Eric J.  
Atty. Docket No.: R-0003 US  
Title: Technique for Programming Floating-gate Transistor Used in  
Circuitry Such as Flash EPROM  
Assignee(s): ProMOS Technologies Inc.

Mountain View, California  
18 July 2006

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**AMENDMENT TO TEXT UNDER 37 CFR 1.312**

Sir:

Responsive to the Notice of Allowance mailed 19 April 2006 and under the provisions of 37 CFR 1.312, please amend the above patent application in the following manner.

Ronald J. Meetin  
Attorney at Law  
210 Central Avenue  
Mountain View, CA  
94043-4869  
Tel.: 650-964-9767  
Fax: 650-964-9779